



# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## In re Application of :

Hung Nguyen

### Serial No. :

10/002,817

### Group Art Unit :

2183

### Filed :

November 02, 2001

### Examiner :

Tsai, Henry W.H.

### For :

Mechanism And Method For  
Reducing Pipeline Stalls Between  
Nested Calls and Digital Signal  
Processor Incorporating The Same

### Atty Docket :

/ 01-629

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

12/22/05  
Date

Signature

## SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

### Official Draftsman

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milipitas, CA 95035  
408-433-7475

Respectfully submitted,

Sandeep Jaggi

Reg. No. 43,331

Date: 12/22/05